

CLAIMS

- 1 1. A transceiver system for use in a telecommunication system, said transceiver
2 system comprising:
3 a transmission circuit including a differential transmitter input coupled to a differential
4 input of a transmission amplifier;
5 a receiver circuit including a differential receiver output coupled to a differential output
6 of a receiver amplifier; and
7 a transmission line interface circuit coupled to a differential output of said transmission
8 amplifier and to a differential input of said receiver amplifier, said transmission line interface
circuit providing a second order high pass transfer function.
2. The system as claimed in claim 1, wherein said transmission line interface circuit
includes two matching impedances, each of which is connected in series in a feedback path of the
transmission amplifier.
3. The system as claimed in claim 1, wherein said transmission circuit includes a
pair of capacitors, each of which is coupled to a feedback path of said transmission amplifier.
- 1 4. A transceiver system for use in a telecommunication system, said transceiver
2 system comprising:
3 a transmission circuit including a differential transmitter input coupled to a differential
4 input of a transmission amplifier;

5 a receiver circuit including a differential receiver output coupled to a differential output
6 of a receiver amplifier; and
7 a transmission line interface circuit coupled to a differential output of said transmission
8 amplifier and to a differential input of said receiver amplifier, said transmission line interface
9 circuit including a two pairs of feedback paths for said transmission amplifier, two of which
10 feedback paths include capacitors.

1 5. The system as claimed in claim 4, wherein said transmission circuit includes two
2 matching impedances, each of which is connected in series with one of each path in the
3 differential output of the transmission amplifier.

4 6. The system as claimed in claim 4, wherein said transmission circuit includes two
5 matching impedances, each of which is connected in series with one of said two pairs of
6 feedback paths of said transmission amplifier.

7 7. The system as claimed in claim 6, wherein said matching impedances are
8 connected in series with said feedback paths that include capacitors.

1 8. A line driver circuit for use in a transceiver system, said circuit comprising a
2 transmission line interface circuit that is coupled to an output of a transmission amplifier and to
3 an input of a receiver amplifier, said transmission line interface circuit providing a second order
4 high pass transfer function.

1 9. The circuit as claimed in claim 8, wherein said transmission line interface circuit
2 includes a pair of capacitors, each of which is provided in a feedback path of said transmission
3 amplifier.

1 10. The circuit as claimed in claim 8, wherein said transmission line interface circuit
2 includes two matching impedances, each of which is coupled to a feedback path of said
3 transmission amplifier.

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